2015

RENICE X1 Plus CFast Datasheet



Renice Technology Co., Limited 2015-04-10



CATALOGUE

1. Introduction	3
1.1 Product Overview	3
1.2 Feature	3
1.3 Functional Block Diagram	4
1.4 Form Size Without Write Protect Switch:	4
1.5 Form Size With Write Protect Switch:	5
2. Pinout Information	5
2.1 Pin Assignment	5
2.2 Connector Pin Signal Definitions	6
3. Power Specification	7
3.1 Operating Voltage: 3.3V (±5%)	7
3.2 Power Supply Voltage	7
3.3 Power Consumption (typical)	7
4. Reliability Specification	7
4.1 Environmental Specifications	7
4.2 Power Failure Protection	7
4.3 Over voltage Protection	8
5. SATA Interface Features	8
6. ATA Command	9
6.1 ATA Command Set	9
6.2 ATA Command Description: IDENTIFY DEVICE (ECh)	10
6.3 ATA Command Description: SET FEATURES (EFh)	16
7. S.M.A.R.T	17
8. Electrical Characteristics	18
8.1 Absolute Maximum Rating	18
8.2 Recommended Operation Condition	18
8.3 DC Characteristics	19
9. Security Function (Optional)	
10. Write Protect Function (Optional)	
11. Ordering Information	
12. Part Number Naming Rule	21

1. Introduction

1.1 Product Overview

RENICE X1 Plus CFast Storage Card is a compact storage card with SATAIII 6.0Gb/s interface, it is compliant with the CFast 1.0 standard and ATA protocol, offers high performance, reliability and low power consumption to next generat ion embedded devices.

1.2 Feature

• Performance:

Sequential Data Read up to: 420MB/s Sequential Data Write up to: 180MB/s

Access Time: 0.1ms

- Form factor: 42.8mmX36.4mmX3.6mm (LxWxH)
- Weight:<10g
- Interface standard: 7+17pin SATAIII 6.0Gb/s
- Density: 8GB~128GB (MLC)
 2GB~64GB (SLC)
- Input voltage: 3.3V (±5%)
- Operation Temp.: $0 \sim +70^{\circ}\mathbb{C}$ (Commercial) $-40 \sim +85^{\circ}\mathbb{C}$ (Industrial) Storage Temp.: -55 to $+95^{\circ}\mathbb{C}$
- Flash management algorithm: static and dynamic wear-leveling, bad block management algorithm
- Supports dynamic power management and SMART (Self-Monitoring, Analysis and Reporting Technology)
- Supports BCH ECC up to 40bits in 1Kbytes
- Supports NCQ,
- Supports Trim
- Supports power failure protect function
- Supports overvoltage protect function
- Support Write Protect Function (Optional)
- Support Quick Erase (Optional)
- Data retention: JESD47 compliant
- MTBF: >3,000,000 Hours



1.3 Functional Block Diagram

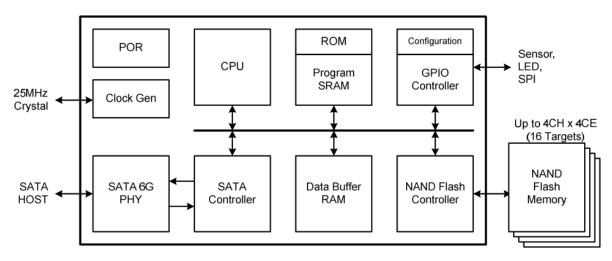


Figure 1: Functional Block Diagram

1.4 Form Size Without Write Protect Switch:

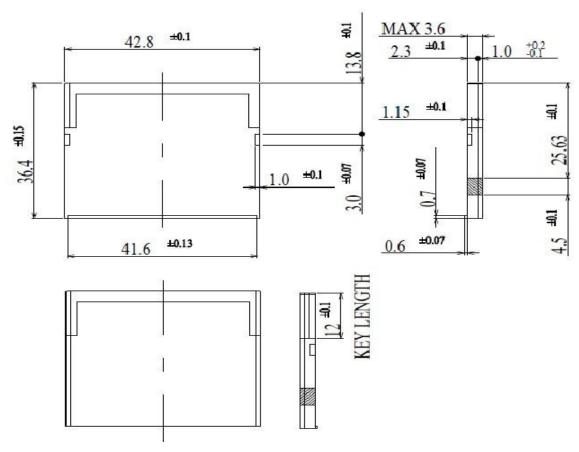


Figure 2: Form size without write protect switch

1.5 Form Size With Write Protect Switch:

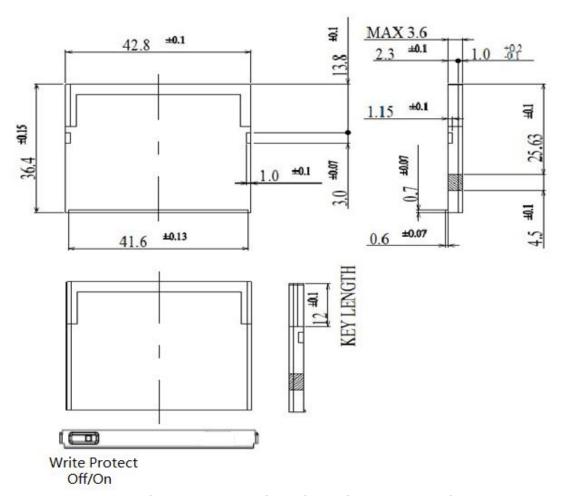


Figure 3: Form size with write protect switch

2. Pinout Information

2.1 Pin Assignment

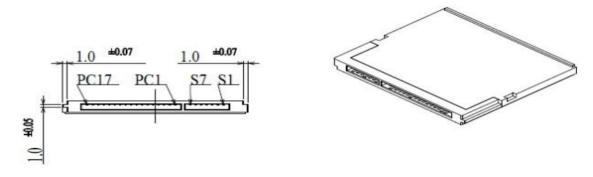


Figure 4: Pin Assignment



2.2 Connector Pin Signal Definitions

Table 1:Connector Pin Signal Definitions

Pin Segment		Name	Type	Description	Mate Sequence	
S1	SATA	SGND	Signal GND	Vss, Ground for signal integrity	1st	
S2	SATA	A+	SATA Differential		2nd	
S3	SATA	A-	SATA Differential	HOST, Signal Pair A	2nd	
S4	SATA	SGND	Signal GND	Vss, Ground for signal integrity	1st	
S5	SATA	B-	SATA Differential	HOST, Signal Pair B	2nd	
S6	SATA	B+	SATA Differential	11031, Signal Pall B	2nd	
S7	SATA Key	SGND	Signal GND	Vss, Ground for signal integrity	1st	
PC1	Key PWR/CTL	CDI	CMOS Input ¹	Card Detect In (CDI)	3rd	
PC2	PWR/CTL	GND	Device GND	Vss	1st	
PC3	PWR/CTL	TBD	Do not use	Do not use		
PC4	PWR/CTL	TBD	No Connect	(reserved)	6	
PC5	PWR/CTL	TBD	Quick Erase	Quick Erase		
PC6	PWR/CTL	TBD	No Connect	(reserved)		
PC7	PWR/CTL	GND	Device GND	Vss	1st	
PC8	PWR/CTL	1011	No Connect	Reserved Input/Output	2nd	
PC9	PWR/CTL	101 2	DAS	DAS	2nd	
PC10	PWR/CTL	101	Write Protect	Write Protect	15	
PC11	PWR/CTL	102	No Connect	Reserved Input/Output		
PC12	PWR/CTL	103	No Connect	Reserved Input/Output	8).	
PC13	PWR/CTL	PWR	3.3V	Device Power (3.3V)		
PC14	PWR/CTL	PWR	3.3V	Device Power (3.3V)		
PC15	PWR/CTL	PGND	Device GND	Vss 1s		
PC16	PWR/CTL	PGND	Device GND	Vss 1st		
PC17	PWR/CTL	CDO	CMOS Output1	Card Detect Out (CDO) 3rd		

3. Power Specification

3.1 Operating Voltage: 3.3V (±5%)

3.2 Power Supply Voltage

1.2v for Core, 3.3V for NAND and IO.

3.3 Power Consumption (typical)

Operation (Read/Write) –1.0W Idle - 0.225W Standby - 0.45W Sleep (Partial/Slumber) - 0.45W/0.2W

4. Reliability Specification

4.1 Environmental Specifications

Table 2: Reliability Specifications

Item		Features						
	Operation	Commercial: 0~70°C						
Temperature	Operation	Industrial: -40~+85°C						
	Storage	-55~+95°C						
Humidity	5-95%	5-95%						
Vibration	10Hz-2000Hz, 16.4 G (X, Y, Z axis, 1 hour /axis)							
Shock	Peak Acceleration: 1,500 G, 0.5ms(Half-sine wave, \pm X, \pm Y, \pm Z axis, 1 time/axis)							
SHOCK	Peak Acceleration: 50 G, 11ms(Half-sine wave, $\pm X, \pm Y, \pm Z$ axis, 3 times/axis)							

4.2 Power Failure Protection

Renice X1 Plus CFast adopts Voltage Detector Circuit to detect current voltage status, when current voltage is detected abnormal, the controller will stop the data to be written into the disk, and ensure the existed data integrity upon sudden power loss.



4.3 Over voltage Protection

The over voltage protection mechanism of Renice X1 Plus CFast is designed to be a protect circuitry on Device Power In. Once the voltage is exceeded, it will be pull down to the normal value in very short time to protect the drive.

5. SATA Interface Features

- . Compliant with Serial ATA International Organization: Serial ATA Revision 3.1.
- . Supports 1-port 1.5/3.0/6.0Gbps (SATA I/II/III interface)
- . Supports ATA-8 command set
- . Supports Partial/Slumber/Device Sleep and dynamic power management
- . Native Command Queuing (NCQ) support
- . S.M.A.R.T. command transport (SCT) technology



6. ATA Command

6.1 ATA Command Set

This table with the following paragraphs summarizes the ATA command set.

Table 3: ATA Command List

Code				P	ARAMET	ERS USE	D	
EXECUTE DIAGNOSTICS 90h X	Command Name	Code	sc	SN	CY	DR	HD	FT
FLUSH CACHE	CHECK POWER MODE	E5h	Х	Х	Х	0	Х	Х
IDENTIFY DEVICE	EXECUTE DIAGNOSTICS	90h	Х	Х	Х	0	Х	Х
IDLE	FLUSH CACHE	E7h	Х	Х	Х	0	0	Х
IDLE IMMEDIATE	IDENTIFY DEVICE	ECh	X	X	X	0	X	X
INITIALIZE DEVICE PARAMETERS	IDLE	E3h	0	X	X	0	Х	X
READ DMA C8h O O O O X READ DMA EXT 25h O O O O X READ FPDMA QUEUED 60h O O O O O X READ LOG DMA EXT 47h O O O O O X READ LOG EXT 27h O O O O O X READ MULTIPLE C4h O O O O O X READ SECTOR(S) 20h or 21h O O O O X READ VERIFY SECTOR(S) 40h or 41h O O O O X READ LOG EXT 10h X	IDLE IMMEDIATE	E1h	X	X	X	0	X	X
READ DMA EXT 25h 0 0 0 0 X READ FPDMA QUEUED 60h 0 0 0 0 0 X READ LOG DMA EXT 47h 0 0 0 0 0 X READ LOG EXT 2Fh 0 0 0 0 0 X READ MULTIPLE C4h 0 0 0 0 0 X READ SECTOR(S) 20h or 21h 0 0 0 0 0 X READ VERIFY SECTOR(S) 40h or 41h 0 0 0 0 X X RECALIBRATE 10h X <td>INITIALIZE DEVICE PARAMETERS</td> <td>91h</td> <td>0</td> <td>Х</td> <td>X</td> <td>0</td> <td>0</td> <td>X</td>	INITIALIZE DEVICE PARAMETERS	91h	0	Х	X	0	0	X
READ FPDMA QUEUED 60h O O O O O X READ LOG DMA EXT 47h O O O O O X READ LOG EXT 2Fh O O O O O X READ MULTIPLE C4h O O O O O X READ SECTOR(S) 20h or 21h O O O O O X READ VERIFY SECTOR(S) 40h or 41h O O O O X X RECALIBRATE 10h X <t< td=""><td>READ DMA</td><td>C8h</td><td>0</td><td>0</td><td>0</td><td>0</td><td>0</td><td>Х</td></t<>	READ DMA	C8h	0	0	0	0	0	Х
READ LOG DMA EXT 47h O O O O X READ LOG EXT 2Fh O O O O X READ MULTIPLE C4h O O O O O X READ SECTOR(S) 20h or 21h O O O O O X READ VERIFY SECTOR(S) 40h or 41h O O O O X X RECALIBRATE 10h X X X X O X X SECURITY DISABLE PASSWORD F6h X	READ DMA EXT	25h	0	0	0	0	0	X
READ LOG EXT 2Fh O O O O X READ MULTIPLE C4h O O O O O X READ SECTOR(S) 20h or 21h O O O O O X READ VERIFY SECTOR(S) 40h or 41h O O O O X X RECALIBRATE 10h X X X O X X X S O X	READ FPDMA QUEUED	60h	0	0	0	0	0	X
READ MULTIPLE C4h O O O O X READ SECTOR(S) 20h or 21h O O O O X READ VERIFY SECTOR(S) 40h or 41h O O O O X RECALIBRATE 10h X X X O X X SECURITY DISABLE PASSWORD F6h X X X O X X SECURITY ERASE PREPARE F3h X X X O X X SECURITY ERASE UNIT F4h X X X X O X X SECURITY FREEZE LOCK F5h X X X X O X X SECURITY SET PASSWORD F1h X	READ LOG DMA EXT	47h	0	0	0	0	0	X
READ SECTOR(S) 20h or 21h O O O O X READ VERIFY SECTOR(S) 40h or 41h O O O O X RECALIBRATE 10h X X X X X X SECURITY DISABLE PASSWORD F6h X	READ LOG EXT	2Fh	0	0	0	0	0	Х
READ VERIFY SECTOR(S) 40h or 41h O O O O X RECALIBRATE 10h X	READ MULTIPLE	C4h	0	0	0	0	0	X
RECALIBRATE 10h X X X O X X SECURITY DISABLE PASSWORD F6h X X X O X X SECURITY ERASE PREPARE F3h X X X O X X SECURITY ERASE UNIT F4h X X X O X X SECURITY FREEZE LOCK F5h X X X O X X SECURITY SET PASSWORD F1h X X X O X X SECURITY UNLOCK F2h X X X O X X SEEK 7xh X X X O O X X SET FEATURES EFh O X X O X X O X X O X X O X X S SET MULTIPLE MODE C6h O X X X X <td>READ SECTOR(S)</td> <td>20h or 21h</td> <td>0</td> <td>0</td> <td>0</td> <td>0</td> <td>0</td> <td>Х</td>	READ SECTOR(S)	20h or 21h	0	0	0	0	0	Х
SECURITY DISABLE PASSWORD F6h X<	READ VERIFY SECTOR(S)	40h or 41h	0	0	0	0	0	Х
SECURITY ERASE PREPARE F3h X <td>RECALIBRATE</td> <td>10h</td> <td>X</td> <td>X</td> <td>X</td> <td>0</td> <td>X</td> <td>X</td>	RECALIBRATE	10h	X	X	X	0	X	X
SECURITY ERASE UNIT F4h X	SECURITY DISABLE PASSWORD	F6h	Х	Х	X	0	X	X
SECURITY FREEZE LOCK F5h X	SECURITY ERASE PREPARE	F3h	Х	X	X	0	X	X
SECURITY SET PASSWORD F1h X	SECURITY ERASE UNIT	F4h	X	X	X	0	X	X
SECURITY UNLOCK F2h X O O X	SECURITY FREEZE LOCK	F5h	Х	X	Х	0	Х	Х
SEEK 7xh X X O O O X SET FEATURES EFh O X X O X O X O O X X O X X O X	SECURITY SET PASSWORD	F1h	Х	Х	Х	0	X	Х
SET FEATURES EFh O X X O X O SET MULTIPLE MODE C6h O X X O X X SLEEP E6h X X X X O X X SMART B0h X X X O O X O STANDBY E2h X X X X X X X STANDBY IMMEDIATE E0h X <	SECURITY UNLOCK	F2h	X	X	X	0	X	X
SET MULTIPLE MODE C6h O X X O X X SLEEP E6h X X X O X X SMART B0h X X O O X O STANDBY E2h X X X X O X X STANDBY IMMEDIATE E0h X X X X O X X WRITE DMA CAh O O O O O X X WRITE DMA EXT 35h O O O O X X WRITE LOG DMA EXT 57h O O O O X X WRITE MULTIPLE C5h O O O O O X	SEEK	7xh	Х	X	0	0	0	X
SLEEP E6h X </td <td>SET FEATURES</td> <td>EFh</td> <td>0</td> <td>X</td> <td>X</td> <td>0</td> <td>X</td> <td>0</td>	SET FEATURES	EFh	0	X	X	0	X	0
SMART B0h X X O O X O STANDBY E2h X	SET MULTIPLE MODE	C6h	0	X	Х	0	X	Х
STANDBY E2h X	SLEEP	E6h	Х	Х	Х	0	Х	Х
STANDBY IMMEDIATE E0h X X X X X X WRITE DMA CAh O O O O O X WRITE DMA EXT 35h O O O O O X WRITE FPDMA QUEUED 61h O O O O O X WRITE LOG DMA EXT 57h O O O O O X WRITE LOG EXT 3Fh O O O O O X WRITE MULTIPLE C5h O O O O O X	SMART	B0h	Х	Х	0	0	Х	0
WRITE DMA CAh O O O O X WRITE DMA EXT 35h O O O O O X WRITE FPDMA QUEUED 61h O O O O O X WRITE LOG DMA EXT 57h O O O O X WRITE LOG EXT 3Fh O O O O X WRITE MULTIPLE C5h O O O O X	STANDBY	E2h	Х	Х	Х	0	Х	Х
WRITE DMA EXT 35h O O O O X WRITE FPDMA QUEUED 61h O O O O X WRITE LOG DMA EXT 57h O O O O X WRITE LOG EXT 3Fh O O O O X WRITE MULTIPLE C5h O O O O X	STANDBY IMMEDIATE	E0h	Х	Х	Х	0	Х	Х
WRITE FPDMA QUEUED 61h O O O O X WRITE LOG DMA EXT 57h O O O O X WRITE LOG EXT 3Fh O O O O X WRITE MULTIPLE C5h O O O O X	WRITE DMA	CAh	0	0	0	0	0	Х
WRITE FPDMA QUEUED 61h O O O O X WRITE LOG DMA EXT 57h O O O O X WRITE LOG EXT 3Fh O O O O X WRITE MULTIPLE C5h O O O O X			+			0	0	
WRITE LOG DMA EXT 57h O O O O X WRITE LOG EXT 3Fh O O O O X WRITE MULTIPLE C5h O O O O X								
WRITE LOG EXT 3Fh O O O O X WRITE MULTIPLE C5h O O O O X			0				 	—
WRITE MULTIPLE C5h O O O O X								
	WRITE SECTOR(S)	30h or 31h	0	0	0	0	0	X

Note: O = Valid, X = Don't care; SC = Sector Count Register; SN = Sector Number Register;



CY = Cylinder Low/High Register; DR = DEVICE SELECT Bit (DEVICE/HEAD Register Bit 4);

HD = HEAD SELECT Bit (DEVICE/HEAD Register Bit 3-0); FT = Features Register.

6.2 ATA Command Description: IDENTIFY DEVICE (ECh)

This commands read out 512Bytes of drive parameter information. Parameter Information consists of the arrangement and value as shown in the following table. This command enables the host to receive the Identify Drive Information from the device.

Table 4 IDENTIFY DEVICE (ECh) information default value

Word	Value	F/V	Description		
0	0040h	F X F X F	General configuration bit-significant information: 15		
1	XXXXh	Х	Number of logical cylinders		
2	C837h	V	Specific configuration		
3	00XXh	х	Number of logical heads		
4-5	XXXXh	х	Retired		
6	XXXXh	х	Number of logical sector per logical track		
7-8	XXXXh	V	Reserved for assignment by the CompactFlash_ Association		
9	000Eh	Х	Retired		
10-19	XXXXh	F	Serial number (20 ASCII characters)		
20-21	XXXXh	х	Retired		
22	003Fh	х	Obsolete		
23-26	XXXXh	F	Firmware revision (8 ASCII characters)		
27-46	XXXXh	F	Model number (40 ASCII characters)		



Word	Value	F/V	Description
		F	15-8 80h
47	8000h	F	7-0 00h = Reserved
		F	01h = Maximum number of 1 sectors on READ/WRITE MULTIPLE commands
48	4000h	F	Reserved
			Capabilities
		F	15-14 Reserved for the IDENTIFY PACKET DEVICE command.
		F	13 1 = Standby timer values as specified in this standard are supported
			0 = Standby timer values shall be managed by the device
	.=	F	12 Reserved for the IDENTIFY PACKET DEVICE command.
49	2F00h	F	11 1 = IORDY supported
			0 = IORDY may be supported
		F	10 1 = IORDY may be disabled
		F	9 1 = LBA supported
		F	8 1 = DMA supported.
		X	7-0 Retired
			Capabilities
		F	15 Shall be cleared to zero.
50	4000h	F	14 Shall be set to one.
		F	13-2 Reserved.
		X	1 Obsolete
		F	Shall be set to one to indicate a device specific Standby timer value minimum.
51-52	0000h	X	Obsolete
		F	15-3 Reserved
		F	2 1 = the fields reported in word 88 are valid
			0 = the fields reported in word 88 are not valid
53	0007h	F	1 1 = the fields reported in words 70:64 are valid
		l	0 = the fields reported in words 70:64 are not valid
		X	0 1 = the fields reported in words 58:54 are valid
			0 = the fields reported in words 58:54 are not valid
54-58	XXXXh	X	Obsolete
		F	15-9 Reserved
59	0000h	V	8 1 = Multiple sector setting is valid
		\ \	7-0 xxh = Setting for number of sectors that shall be transferred per interrupt on R/W Multiple command
60-61	XXXXh	F	Total number of user addressable sectors
62	0000h	х	Obsolete
		F	15-11 Reserved
		V	10 1 = Multiword DMA mode 2 is selected
			0 = Multiword DMA mode 2 is not selected
63	0007h	V	9 1 = Multiword DMA mode 1 is selected
"	300711		0 = Multiword DMA mode 1 is not selected
		V	8 1 = Multiword DMA mode 0 is selected
			0 = Multiword DMA mode 0 is not selected
		F	7-3 Reserved



Word	Value	F/V	Description
		F F	2 1 = Multiword DMA mode 2 and below are supported 1 1 = Multiword DMA mode 1 and below are supported 0 1 = Multiword DMA mode 0 is supported
64	0003h	F F	15-8 Reserved 7-0 Advanced PIO modes supported
65	0078h	F	Minimum Multiword DMA transfer cycle time per word
66	0078h	F	Manufacturer's recommended Multiword DMA transfer cycle time
67	0078h	F	Minimum PIO transfer cycle time without flow control
68	0078h	F	Minimum PIO transfer cycle time with IORDY flow control
69-74	0000h	F	Reserved (for future command overlap and queuing)
		F	Queue depth
75	0000h		15:5 Reserved
			4:0 Maximum queue depth - 1
		F	Serial ATA Capabilities
			15:13 Reserved for Serial ATA
			12 1 = Supports NCQ priority information
			11 1 = Supports Unload while NCQ commands are outstanding
			10 1 = Supports the SATA Phy Event Counters log
70	Lab.		9 1 = Supports receipt of host initiated power management requests
76	xh		8 1 = Supports the NCQ feature set
			7:4 Reserved for Serial ATA
			3 1 = Supports SATA Gen3 Signaling Speed (6.0Gb/s)
			2 1 = Supports SATA Gen2 Signaling Speed (3.0Gb/s)
			1 1 = Supports SATA Gen1 Signaling Speed (1.5Gb/s)
			0 Shall be cleared to zero
77			Reserved
			Serial ATA features supported
			15:7 Reserved for Serial ATA
			6 1 = Device supports Software Settings Preservation
78	xh		5 Reserved for Serial ATA
			4 1 = Device supports in-order data delivery
			3 1 = Device supports initiating power management
			2 1 = Device supports DMA Setup auto-activation



Word	Value	F/V	Description
			1 1 = Device supports non-zero buffer offsets
			0 Shall be cleared to zero
			Serial ATA features enabled
			15:7 Reserved for Serial ATA
			6 1 = Software Settings Preservation enabled
			5 Reserved for Serial ATA
79	xh		4 1 = In-order data delivery enabled
/ 9	All		
			3 1 = Device initiated power management enabled
			2 1 = DMA Setup auto-activation enabled
			1 1 = Non-zero buffer offsets enabled
			F 0 Shall be cleared to zero
			Major version number 0000h or FFFFh = device does not report version
		F	15 Reserved
		F	14 Reserved for ATA/ATAPI-14
		F	13 Reserved for ATA/ATAPI-13
		F	12 Reserved for ATA/ATAPI-12
		F	11 Reserved for ATA/ATAPI-11
		F	10 Reserved for ATA/ATAPI-10
		F	9 Reserved for ATA/ATAPI-9
80	01FEh	F	8 Reserved for ATA/ATAPI-8
		F	7 1 = supports ATA/ATAPI-7
		F	6 1 = supports ATA/ATAPI-6
		F	5 1 = supports ATA/ATAPI-5
		F	4 1 = supports ATA/ATAPI-4
		F	3 Obsolete
		X	2 Obsolete
		×	1 Obsolete
		F	0 Reserved
81	0021h	F	Minor version number
			Command set supported.
		X	15 Obsolete
		F	14 1 = NOP command supported
		F	13 1 = READ BUFFER command supported
		F	12 1 = WRITE BUFFER command supported
82	00006	X	11 Obsolete
82	0068h	F	10 1 = Host Protected Area feature set supported
		F	9 1 = DEVICE RESET command supported
		F	8 1 = SERVICE interrupt supported
		F	7 1 = release interrupt supported
		F	6 1 = look-ahead supported
		F	5 1 = write cache supported



Word	Value	F/V	Descrip	otion
		F	4	Shall be cleared to zero to indicate that the PACKET Command feature set is not
		F	supported	1.
		F		1 = mandatory Power Management feature set supported
		F		1 = Removable Media feature set supported
		F	1	1 = Security Mode feature set supported
				1 = SMART feature set supported
				d sets supported.
		F	15	Shall be cleared to zero
		F	14	Shall be set to one
		F	13-9 I	Reserved
		F	8	1 = SET MAX security extension supported
		F	7 1	Reserved
83	5000h	F	6	1 = SET FEATURES subcommand required to spinup after power-up
		F	1	1 = Power-Up In Standby feature set supported
		F		1 = Removable Media Status Notification feature set supported
		F		1 = Advanced Power Management feature set supported
		F		1 = CFA feature set supported
		F	1	1 = READ/WRITE DMA QUEUED supported
		F		1 = DOWNLOAD MICROCODE command supported
				d set/feature supported extension.
		F		Shall be cleared to zero
		F		Shall be set to one
84	4000h	F		Reserved
		F		1 = SMART self-test supported
		F	1	1 = SMART error logging supported
		<u> </u>		d set/feature enabled.
		l x		Obsolete
		l ê		1 = NOP command enabled
		F		1 = READ BUFFER command enabled
		F		1 = WRITE BUFFER command enabled
		1 '		
		X		Obsolete
		\ \rac{1}{2}		1 = Host Protected Area feature set enabled
		F	1	1 = DEVICE RESET command enabled
85	0008h	V		1 = SERVICE interrupt enabled
		l v		1 = release interrupt enabled
		V	"	1 = look-ahead enabled
		V	"	1 = write cache enabled
		F	4 3	Shall be cleared to zero to indicate that the PACKET Command feature set is not
		F	supported	
		F		1 = Power Management feature set enabled
		V	-	1 = Removable Media feature set enabled
		V	1	1 = Security Mode feature set enabled
			0	1 = SMART feature set enabled
		1	Command	d set/feature enabled.
86	5000h	F	15-9 I	Reserved
50	300011	F	8	1 = SET MAX security extension enabled by SET MAX SET PASSWORD
		F	7 :	See Address Offset Reserved Area Boot, INCITS TR27:2001



Word	Value	F/V	Description
		F	6 1 = SET FEATURES subcommand required to spin-up after power-up
		V	5 1 = Power-Up In Standby feature set enabled
		V	4 1 = Removable Media Status Notification feature set enabled
		l v	3-1 1 = Advanced Power Management feature set enabled
		F	0 1 = DOWNLOAD MICROCODE command supported
			Command set/feature default.
		F	15 Shall be cleared to zero
87	4000h	F	14 Shall be set to one
0,	400011	F	13-2 Reserved
		F	1 1 = SMART self-test supported
		F	0 1 = SMART error logging supported
			15-14 Reserved
		l v	13 1 = Ultra DMA mode 5 is selected
			0 = Ultra DMA mode 5 is not selected
		l v	12 1 = Ultra DMA mode 4 is selected
			0 = Ultra DMA mode 4 is not selected
		l v	11 1 = Ultra DMA mode 3 is selected
			0 = Ultra DMA mode 3 is not selected
		l v	10 1 = Ultra DMA mode 2 is selected
			0 = Ultra DMA mode 2 is not selected
88	xh	V	9 1 = Ultra DMA mode 1 is selected
00	A11		0 = Ultra DMA mode 1 is not selected
		F	8 1 = Ultra DMA mode 0 is selected
		F	0 = Ultra DMA mode 0 is not selected
		F	7-6 Reserved
		F	5 1 = Ultra DMA mode 5 and below are supported
		F	4 1 = Ultra DMA mode 4 and below are supported
		F	3 1 = Ultra DMA mode 3 and below are supported
			2 1 = Ultra DMA mode 2 and below are supported
			1 1 = Ultra DMA mode 1 and below are supported
			0 1 = Ultra DMA mode 0 is supported
89	0000h	F	Time required for security erase unit completion
90	0000h	F	Time required for Enhanced security erase completion
91	0000h	V	Current advanced power management value
92	0000h	v	Master Password Revision Code
93	0000h	х	Hardware reset result
94-126	0000h	v	Reserved
			Removable Media Status Notification feature set support
		F	15-2 Reserved
		F.	1-0 00 = Removable Media Status Notification feature set not supported
127	0000h	'	01 = Removable Media Status Notification feature supported
			10 = Reserved
			11 = Reserved



Word	Value	F/V	Description		
			Security status		
		F	15-9 Reserved		
		V	8 Security level 0 = High, 1 = Maximum		
		F	7-6 Reserved		
128	0001h	F	5 1 = Enhanced security erase supported		
120	000111	V	4 1 = Security count expired		
		V	3 1 = Security frozen		
		V	2 1 = Security locked		
		V	1 1 = Security enabled		
		F	0 1 = Security supported		
129-159	0000h	x	Vendor specific		
160-254	0000h	х	Reserved		
			Integrity word		
255	0000h	×	15-8 Checksum		
			7-0 Signature		

Note:

F/V = Fixed/variable content

F = the content of the word is fixed and does not change. For removable media devices, these values may change when media is removed or changed.

V = the contents of the word is variable and may change depending on the state of the device or the commands executed by the device.

X = the content of the word may be fixed or variable.

6.3 ATA Command Description: SET FEATURES (EFh)

This command set parameter to Features register and set drive's operation. For transfer mode, parameter is set to Sector Count Register. This command is used by the host to establish or select certain features.

Table 5 Features Register value and available operating mode

Value	Function
02h	Enable write cache
03h	Set transfer mode based on value in Sector Count register.
55h	Disable read look-ahead feature
82h	Disable write cache
90h	Disable use of SATA feature
AAh	Enable read look-ahead feature



7. S.M.A.R.T

Table 6: SMART Command Set

Value(Hex)	Command		
01	Read Error Rate		
02	Throughput Performance		
03	Spin Up Time		
05	Re-allocated Sector Count		
07	Seek Error Rate		
08	Seek Time Performance		
09	Power-on Hours Count		
0A	Spin Retry Count		
0C	Drive Power Cycle Count		
A7	SSD Protect Mode		
A8	SATA PHY Error Count		
A9	Bad Block Count		
AA	Max Bad Block Count		
AD	Erase Count		
AF	Bad Cluster Count		
B4	Spare Block Count Left		
ВВ	Uncorrectable Error Count		
C0	Unexpected Power Loss Count		
C2	Temperature		
C5	Current Pending Sector Count		
E7	SSD Life Left		
F0	Write Head		
F1	Total LBA written		
F2	Total LBA read		

8. Electrical Characteristics

8.1 Absolute Maximum Rating

Table 7: Absolute Maximum Rating

Parameter	Symbol	Condition	Min	Max	Unit
Analog power supply	AS3V3		-0.4	3.96	٧
Analog power supply	AS1V2		-0.4	1.6	V
Digital core power supply	C1V2		-0.4	1.6	٧
Digital I/O power supply	G3V3		-0.4	3.96	V
Flash I/O power supply	FVDD		-0.4	3.96	V
Digital I/O input voltage	V _{I(D)}		-0.4	G3V3+0.4	V
Storage temperature	T _{STORAGE}		-40	130	°C

8.2 Recommended Operation Condition

Table 8: Recommended Operation Condition

Parameter	Symbol	Condition	Min	Typical	Max	Unit
Analog power supply	AS3V3		2.97	3.3	3.63	V
Analog power supply	A1V2		1.14	1.2	1.32	V
Digital core power supply	C1V2		1.14	1.2	1.32	V
Digital I/O power supply	G3V3		2.97	3.3	3.63	V
Flash I/O power supply at 1.8V	FVDD		1.7	1.8	1.95	V
Flash I/O power supply at 3.3V	FVDD		2.97	3.3	3.63	V
Flash I/O reference voltage	FVREF	1/2 FVDD		0.9		V
Ambient operation temperature	T _A	Commercial Spec.	0	-	70	°C
Ambient operation temperature	T _A	Industrial Spec.	-40	-	85	°C
Junction temperature	TJ				125	°C
Package Surface Temperature	T _C	Commercial Spec.			85	°C
Package Surface Temperature	T _C	Industrial Spec.			100	°C
Thermal Resistance	$\theta_{ extsf{JC}}$			14.53		°C/W

8.3 DC Characteristics

Table 9: DC Characteristics

Parameter	Symbol	Condition	Min	Typical	Max	Unit
Digital I/O Output High Voltage	V_{OH}		2.4			V
Digital I/O Output Low Voltage	V _{OL}				0.4	V
Digital I/O Input High Voltage	V _{IH}	G3V3 = 3.63V	2.2			V
Digital I/O Input Low Voltage	V _{IL}	G3V3 = 2.97V			1.3	V
Weak pull high R value				75.0		kΩ
Weak pull low R value				75.0		kΩ
Power Consumption SATA Active Toshiba NAND flash run at READ: 150MHz, WRITE: 100MHz	AS3V3	AS3V3 = 3.3V		26		mA
	AS1V2	AS1V2 = 1.2V		90		mA
	G3V3	G3V3 = 3.3V		16		mA
	C1V2	C1V2 = 1.2V		235		mA
	FVDD	FVDD = 1.8V		110		mA

9. Security Function (Optional)

Renice X1 Plus CFast supports Quick Erase function by a signal pin(On Renice X1 Plus CFast cards, pin PC5 can be used as Quick Erase signal pin, when PC5 is pulled down, the QE function could be triggered.) for emergency data erasure based on customers' request. Either Renice X1 Plus CFast is acting as master Drive or slave drive, once Quick Erase function is triggered QE will be carried out immediately whether the SSD is in idle mode(no read/write), or work (read/write) mode. After QE is finished, the SSD turns into uninitialized drive and can be used again after formatting.

10. Write Protect Function (Optional)

Renice X1 Plus CFast supports Write Protect function based on customers' request by either a mechanical write protect switch or a signal pin (*On Renice X1Plus CFast cards, pin PC10 could be used as the write protect signal pin. If PC10 is pulled down, WP could be trigged.*) Once write protect function is enabled, Write-protected data in the disk could only be read, and thus users could not write/edit/delete it, or append data to the disk.

Note: The Write Protect function could only be enabled before power on the disk.



11. Ordering Information

Table 10: Valid Combinations

Capacities/Flash type	Standard Temp	Industrial Temp
2GB/SLC	RCS002-SX1PC	RIS002-SX1PC
4GB/SLC	RCS004-SX1PC	RIS004-SX1PC
8GB/SLC	RCS008-SX1PC	RIS008-SX1PC
16GB/SLC	RCS016-SX1PC	RIS016-SX1PC
32GB/SLC	RCS032-SX1PC	RIS032-SX1PC
64GB/SLC	RCS064-SX1PC	RIS064-SX1PC
8GB/MLC	RCM008-SX1PC	RIM008-SX1PC
16GB/MLC	RCM016-SX1PC	RIM016-SX1PC
32GB/MLC	RCM032-SX1PC	RIM032-SX1PC
64GB/MLC	RCM064-SX1PC	RIM064-SX1PC
128GB/MLC	RCM128-SX1PC	RIM128-SX1PC

12. Part Number Naming Rule

